PCD7.L401 Analogue module with 4 outputs, 0...10 VDC



Description

The RIO module was developed as a SBS S-Bus data node for local switching tasks. Via a DDC of the type PCDx / PCS1, inputs can be read and manual/auto function monitored. Two address switches (x1 / x10) on the front panel allow module addressing and identification. Addresses can be set between 00 and 99. Up to 100 RIO modules and a maximum of 3 PCD stations can be connected to one bus branch simultaneously.

Technical data
Bus system
Transmission rate SBC S-Bus 1200...38400 Parity

Transmission mode
Bus length max. 1200 m (without repeater) 24 VDC (18 VDC...32 VDC) <50 mA Nominal voltage UN

Current consumption Power consumption Relative duty cycle 1.2 W 100 % Reaction time

15 ms (from receive data to send data reaction) 550 ms

Operating temperature range Storage temperature range

0°C...+55°C -25°C...+70°C Reverse battery protection of service voltage Protective wiring Input state indicator Yellow LED Green LED for bus activity Function indicator

Status indicator Red LED for bus error mes Inputs electrically isolated Special features Test voltage input / bus 2500 VAC / 50 Hz / 1 min.

Signal outputs

Recovery time

4 × 0...10 VDC 5 mA by 10 VDC (2 kOhm) 10 mV/Digit 0...1000 (2 comma stages) Signale type Output current Accuracy Data range

Housing Protection class IP65

1.5 mm² / spring terminals Plug-in terminal Mounting position any

Weight Housing dimensions

350 g W × H × D: 159 × 41,5 × 120 mm

Joining without space

Mounting and commissioning to be conform with current regulations:

Power-off the installation

Place module onto the place of destination
Cable with max. single wire 1,5 mm² insert into 2.

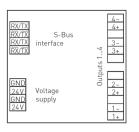
the unit. With consideration of the protection

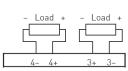
4 Connect the wires into the spring terminals

Connect supply voltage and field bus to the dedicated spring terminals.

Do not exchange the bus and supply spring

PCD7.L401 SAFE





Spring terminals, 1.5 mm², single-wire

Data transmission

All SBC S-Bus instructions (level 1) are recognized. Instructions that have no function in the device are answered with <NAK>. The module has integral, automatic baud rate

Display/Write Register" Register 1 to 4 can be called together (Is is recommended to call them individually)

Adresse	Information
1	Output 1 (devided with 100 => Voltage value)
2	Output 2 (devided with 100 => Voltage value)
3	Output 3 (devided with 100 => Voltage value)
4	Output 4 (devided with 100 => Voltage value)

Remark: The voltage value will be set with a number as (100 => 1 VDC) linear.

"Display Register"	Status register:

Address	<u>Information</u>		Bit 0:	1= Device recognized last transmission
5	Baud rate (plain text => kBit/s)			0= Device did not recognize last transmission
6	Module address		Bit 1:	1= Last transmission was a broadcast
7	Status register			0= Last transmission was not a broadcast
8	Not used		Bit 2:	1= Last transmission came from master
9	Not used			0= Last transmission came from a slave
10 Status register		Bit 3:	1= CRC of last message was correct	
				0= CRC of last message was incorrect
The following registers can be called together		Bit 5:	1= Device has executed an internal reset	
(Display Register "x" to "y") 1 to 4 / 5 to 7			0= Device function is OK	
			Bit 8:	1= Internal bus to EEPROM is OK
"Write Register"				0= Internal bus not working perfectly
Address	<u>Value</u>	Baud rate setting (Baud kbit/s)	Bit 9:	1= EEPROM data memory is OK
5	4	1 200		0= EEPROM data memory is faulty
	5	2 400	Bit 10:	1= Baud rate uploaded from EEPROM
	6	4 800		0= Baud rate is at default value (9600 Bd.)
	7	9 600		
	8	19 200	All other	bits are reserved for factory tests.
	0	20 400		

The write output instruction at address 255 is recognized as broadcast message.

Automatic baud function: "Write or Display output 255" (1 = autobaud active / 0 = autobaud inactive)

After a power failure, the last baud rate set will be reinstalled

For further information on the use of modules linked to S-Bus, including all restrictions, see documentation PP26-339_ENG.